

Abstract of the Disclosure

The invention relates to a memory unit with at least two memory areas for storing data, first terminals for accessing
5 data within the memory areas, and second terminals for accessing data within the memory areas. To provide multi-purpose access to the memory, the memory unit provides at least two access control means for providing selectively
sole addressing and accessing data through one of the
10 terminals, or individual addressing and accessing data through each of the terminals, respectively.

For publication: Figure 2.